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01/18/2002

JCS88

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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10054471	01/18/2002	326		2819	A. TRAN

\*\*APPLICANTS: Song Seungyoon;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/609,490 07/05/2000 PAT 6,348,812

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		1779CIP
Verified and Acknowledged Examiners's initials		
TITLE : Dynamic programmable logic array that can be reprogrammed and a method of use		
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Primary Examiner		Print Fig.	
PREPARED FOR ISSUE		Application Examiner	
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ISSUE FEE	
Amount Due	Date Paid

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☐ CD-ROM

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